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Hao-Yun Chin; Chao-Chung Cheng; Yu-Kun Lin; Tian-Sheuan Chang;
[Design Automation Conference, 2005. Proceedings of the ASP-DAC 2005. Asia and South Pacific Volume 2, 18-21 Jan. 2005 Page\(s\):D/7 - D/8 Vol. 2](#)
 Digital Object Identifier 10.1109/ASPDAC.2005.1466601

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☐ 2. **Low Power Cache Algorithm and Architecture Design for Fast Motion Estimation in H.264/AVC Encoder System**

Chuan-Yuan Tsai; Chen-Han Chung; Yu-Han Chen; Tung-Chien Chen; Liang-Gee Chen;
[Acoustics, Speech and Signal Processing, 2007. ICASSP 2007. IEEE International Conference on Volume 2, 15-20 April 2007 Page\(s\):II-97 - II-100](#)
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